This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

Claims 1-20 (canceled)

Claim 21 (currently amended): A high efficiency switching amplifier amplifying a reference input signal, for digitally processing electric power from a DC direct current supply thereof to a loudspeaker which has a positive terminal and a negative terminal, the switching amplifier comprising:

a first <u>isolated</u> switching power converter comprising bi-directional switches for supplying a voltage proportional to the reference input signal to the positive terminal of the loudspeaker when the amplitude of the reference input signal is positive;

a second <u>isolated</u> switching power converter comprising bi-directional switches for supplying a voltage proportional to the reference input signal to the negative terminal of the loudspeaker when the amplitude of the reference input signal is negative[[, and]]; <u>and</u>

a PWM pulse-width modulated controller for controlling the operation of the switches of the first and second <u>isolated switching</u> power converters in such a manner that when the first <u>isolated switching</u> power converter is active during the positive portion of the reference input signal, the switches of the second <u>isolated switching</u> power converter provide a return path for the first <u>isolated switching</u> power converter's current to and from the loudspeaker[[;]] and when the second <u>isolated switching</u> power converter is active during the negative portion of the reference input signal, the switches of the first <u>isolated switching</u> power converter's current to and from the loudspeaker.

Claim 22 (currently amended): The switching amplifier of claim 21 wherein the

switches of the first and second isolated switching power converters are MOSFETs metal-

oxide-semiconductor field-effect transistors.

Claim 23 (currently amended): The switching amplifier of claim 21 wherein the first

and second isolated switching power converters are selected from a group of converters

comprising a buck converter, a forward converter, a push-pull converter, a half-bridge

converter, an asymmetrical half-bridge converter, and a full-bridge converter.

Claim 24 (currently amended): The switching amplifier of claim 21 wherein the first

and second isolated switching power converters share a transformer and switches coupled to

the primary of the transformer.

Claims 25-39 (canceled)

INTERVIEW SUMMARY UNDER 37 CFR §1.133 AND MPEP §713.04

A telephone interview in the above-reference case was conducted on September 6, 2005 between the Examiner and the Applicant's undersigned representative. The non-final office action mailed on June 7, 2005 was discussed. Specifically, the rejections of claims 21-24 in view of U.S. Patent No. 6,762,645 to <u>Grant</u> and the proposed amendments set forth herein were discussed with the intent to place the claims in better condition for allowance or appeal. The Applicant wishes to thank the Examiner for his time and attention in this case.